



Docket No.: GR 98 P 8109 P

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Date: October 24, 2003

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 09/815,659
Applicant : Ralf Arnold et al.
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Art Unit : 2182
Examiner : to be assigned

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Technology Center 2100

Docket No. : GR 98 P 8109 P
Customer No. : 24131

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner for Patents

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

Wen-Mei W. Hwu et al.: "Compiler Technology for Future Microprocessors",
Proceedings of the IEEE, Vol. 83, No. 12, December 1995, pp. 1625-1640;

Henk Neefs et al.: "A Microarchitecture for a fixed length Block Structured instruction set Architecture", *Proceedings of the Eighth IASTED International Conference on Parallel and Distributed Computing and Systems, IASTED/ACTA Press, 1996, pp. 1-6;*

Richard H. Littin et al.: "Block Based Execution and Task Level Parallelism",
Proceedings of the 3rd Australasian Computer Architecture Conference, ACAC'98,

*Perth, February 2-3, 1998, Australian Computer Science Communications, Vol. 20,
No. 4, pp. 57-66.*

Respectfully submitted,



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